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1	BRS	L1	520	adj3 package and (processor	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B
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3	BRS	L8	128	"438"/\$.ccls. and memory adj3 package and (processor control\$3)	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B